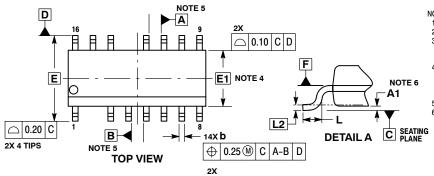
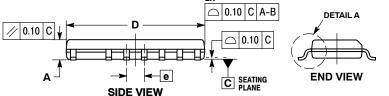


SOIC-16 NB MISSING PINS 2 AND 13

CASE 751DU ISSUE O

DATE 18 OCT 2013





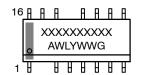
14X

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION.
 ALLOWABLE PROTRUSION SHALL BE 0.10 mm IN EXCESS OF
- MAXIMUM MATERIAL CONDITION.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
 PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS
 OR GATE BURRS SHALL NOT EXCEED 0.25 mm PER SIDE. DIMENSIONS D AND E ARE DETERMINED AT DATUM F.
 DIMENSIONS A AND B ARE TO BE DETERMINED AT DATUM F.
- A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
b	0.35	0.49	
С	0.17	0.25	
D	9.80	10.00	
Е	6.00 BSC		
E1	3.90 BSC		
е	1.27 BSC		
L	0.40	1.27	
L2	0.203 BSC		

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot = Year WW = Work Week = Pb-Free Package G

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

<u> </u>	1.52
1 1.27→ PITCH	7.00 8 14X

RECOMMENDED SOLDERING FOOTPRINT

DOCUMENT NUMBER:	98AON77502F	
STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	SOIC-16 NB MISSING PINS 2 AND 13	

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98AON77502	2F

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ISSUE	REVISION	DATE
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