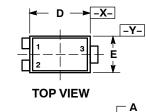
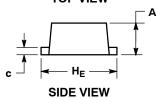


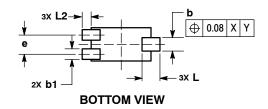
SOT-1123 CASE 524AA ISSUE C

DATE 29 NOV 2011

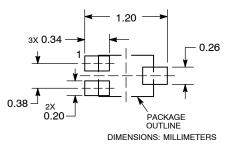
SCALE 8:1







SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- DIMENSIONING AND TOLERANCING PER
- DIMENSIONING AND TOLERANGING FER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD THE ASSET OF THE PROPERTY OF TH
- FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.34	0.40	
b	0.15	0.28	
b1	0.10	0.20	
С	0.07	0.17	
D	0.75	0.85	
E	0.55	0.65	
е	0.35	0.40	
HE	0.95	1.05	
Ĺ	0.185 REF		
L2	0.05	0.15	

GENERIC MARKING DIAGRAM*



= Specific Device Code

= Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
2. EMITTER	2. N/C	2. ANODE	CATHODE	2. SOURCE
COLLECTOR	CATHODE	CATHODE	3. ANODE	DRAIN

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DESCRIPTION:	SOT-1123, 3-LEAD, 1.0X0.6X0.37, 0.35	P	PAGE 1 OF 2



DOCUMENT	NUMBER:
98AON23134	ID

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY D. TRUHITTE.	26 JUL 2006
Α	ADDED STYLES 1 – 5. REQ. BY W. MEADOWS.	08 NOV 2007
В	UPDATED DIMENSION b AND e VALUES. REQ. BY D. TRUHITTE.	18 APR 2008
С	UPDATED DRAWING TO ADD BOTTOM VIEW AND L2 DIMENSION. BY D. TRU- HITTE.	29 NOV 2011

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