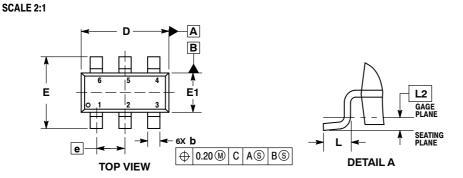
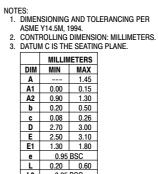
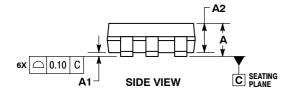
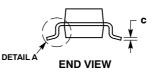


DATE 29 FEB 2012

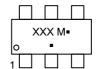








GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

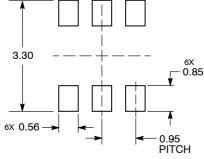
M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

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DESCRIPTION:	SOT-23, 6 LEAD		PAGE 1 OF 2

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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ISSUE	REVISION	DATE	
0	RELEASED FOR PRODUCTION FROM POD #SOT236-029-01 TO ON SEMICONDUCTOR. REQ. BY B. BERGMAN.	19 DEC 2008	
Α	REDRAWN TO JEDEC STANDARDS. REQ. BY D. TRUHITTE.	12 JAN 2011	
В	CORRECTED LABEL FOR MOLDED BODY DIMENSION OF TOP VIEW TO E1. REQ. BY D. TRUHITTE.	29 FEB 2012	

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