

SOT-723 CASE 631AA-01 ISSUE D

**DATE 10 AUG 2009** 

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS.
   MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
   DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
С	0.07	0.12	0.17
D	1.15	1.20	1.25
Е	0.75	0.80	0.85
е	0.40 BSC		
ΗE	1.15	1.20	1.25
L	0.29 REF		
12	0.15	0.20	0.25

## **GENERIC MARKING DIAGRAM\***

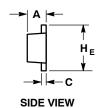
XX= Specific Device Code

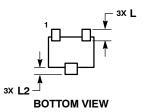
Μ = Date Code



\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

# -Xb1 -Y-2X b 2X e ⊕ 0.08 X Y **TOP VIEW**

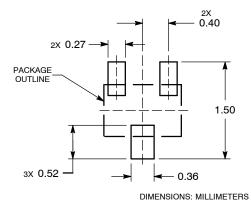




STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE

STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

## **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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NEW STANDARD:			
DESCRIPTION:	SOT-723	PAC	GE 1 OF 2



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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY D. TRUHITTE.	07 AUG 2003
Α	CHANGED DIMS A, b, b1, C, AND D. REQ. BY D. TRUHITTE.	11 NOV 2003
В	CHANGED DIMS b AND b1. REQ. BY D. TRUHITTE.	15 MAR 2004
С	ADDED STYLE 5. REQ. BY S. WINSTON.	01 AUG 2006
D	ADDED BOTTOM VIEW. CHANGED DIMENSION L TO L2. ADDED NEW L DIMENSION. UPDATED SOLDER FOOTPRINT. REQ. BY D. TRUHITTE.	10 AUG 2009

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