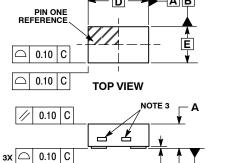


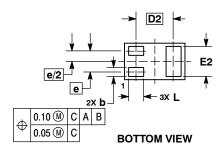
SOT-883 (XDFN3), 1.0x0.6, 0.35P CASE 506CB **ISSUE A**

DATE 30 MAR 2012

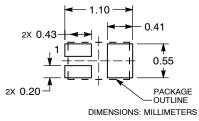




SIDE VIEW



RECOMMENDED SOLDER FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. EXPOSED COPPER ALLOWED AS SHOWN.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.340	0.440	
A1	0.000	0.030	
b	0.075	0.200	
D	0.950	1.075	
D2	0.620 BSC		
е	0.350 BSC		
E	0.550	0.675	
E2	0.425	0.550	
L	0.170	0.300	

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

= Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

DOCUMENT NUMBER:	98AON65407E	Electronic versions
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from versions are unco
NEW STANDARD:		"CONTROLLED CO
DESCRIPTION:	SOT-883 (XDFN3), 1.0X0.6, 0.35P	

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PAGE 1 OF 2



DOCUMENT	NUMBER:
98AON65407	7E

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY I. CAMBALIZA.	17 NOV 2011
Α	CHANGED DESCRIPTION TO SOT-883 (XDFN3) AND CORRECTED MARKING DIAGRAM AND DIMENSION D2. REQ. BY I. CAMBALIZA.	30 MAR 2012

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