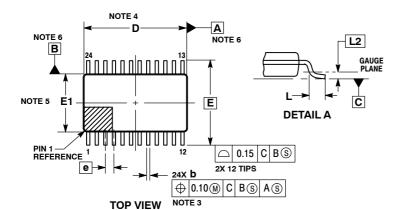
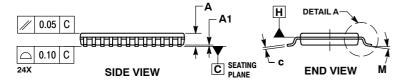


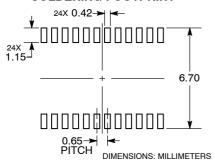
## TSSOP24 7.8x4.4, 0.65P CASE 948H **ISSUE B**

**DATE 21 JUN 2012** 





## RECOMMENDED **SOLDERING FOOTPRINT**

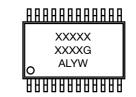


## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION.
  DAMBAR PROTRUSION SHALL BE 0.08 MAX AT MMC. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.
- DIMENSION D DOES NOT INCLUDE MOLD FLASH. PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSION D IS DETERMINED AT DATUM PLANE H.
- DIMENSION E1 DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 PER SIDE. DIMENSION E1 IS DETERMINED AT DATUM PLANE H.
- DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.
   A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEAT-
- ING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

	MILLIMETERS		
DIM	MIN	MAX	
Α		1.20	
A1	0.05	0.15	
b	0.19	0.30	
С	0.09	0.20	
D	7.70	7.90	
E	6.40 BSC		
E1	4.30	4.50	
е	0.65 BSC		
L	0.50	0.75	
L2	0.25 BSC		
M	0°	8°	

## **GENERIC MARKING DIAGRAM\***



= Specific Device Code = Assembly Location

= Wafer Lot L Υ = Year W = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.

DOCUMENT NUMBER:	98ASH70248A	Electro
STATUS:	ON SEMICONDUCTOR STANDARD	access version
NEW STANDARD:		"CON
DESCRIPTION:	TSSOP24 7.8X4.4, 0.65P	

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98ASH70248	A

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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION.	01 JAN 1994
Α	ADDED MARKING DIAGRAM. REQ. BY D. TRUHITTE.	18 APR 2001
В	ADDED RECOMMENDED MOUNTING FOOTPRINT. REQ. BY I. CAMBALIZA.	21 JUN 2012

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