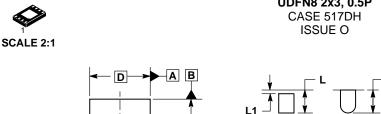
С

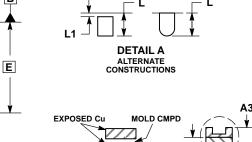
△ 0.05 C

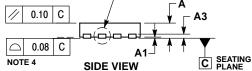
PIN ONE INDICATOR

△ 0.05

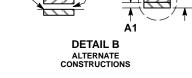
DATE 06 NOV 2015

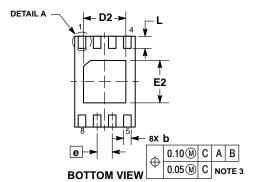






TOP VIEW





RECOMMENDED SOLDERING FOOTPRINT*

UDFN8 2x3, 0.5P

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION b APPLIES TO PLATED
 TERMINAL AND IS MEASURED BETWEEN 1.15 AND 0.25MM FROM THE TERMINAL TIP.
 COPLANARITY APPLIES TO THE EXPOSED
 PAD AS WELL AS THE TERMINALS.
 FOR DEVICE OPN CONTAINING W OPTION,
- DETAIL B ALTERNATE CONSTRUCTION IS NOT APPLICABLE.

	MILLIMETERS	
DIM	MIN	MAX
Α	0.45	0.55
A1	0.00	0.05
A3	0.13 REF	
b	0.20	0.30
D	2.00 BSC	
D2	1.30	1.50
E	3.00 BSC	
E2	1.30	1.50
е	0.50 BSC	
L	0.30	0.50
L1		0.15

GENERIC MARKING DIAGRAM*



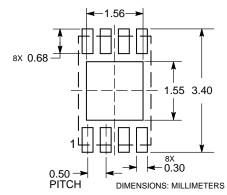
XXXXX = Specific Device Code Α = Assembly Location

WL = Wafer Lot

= Year W = Work Week

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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