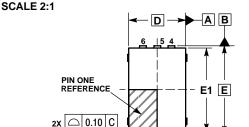
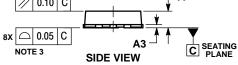
#### WDFN6 2.6x4.0, 0.65P, Dual Flag CASE 511BZ **ISSUE B**

**DATE 02 NOV 2016** 

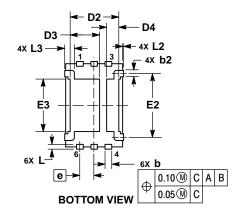


□ 0.10 C





12 3



#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. PROFILE TOLERANCE APPLIES TO THE EXPOSED PADS AS WELL AS THE LEADS.

	MILLIMETERS		
DIM	MIN	MAX	
Α		0.80	
A3	0.10	0.25	
b	0.25	0.40	
b2	0.15	0.30	
D	2.60 BSC		
D2	2.075	2.375	
D3	1.20	1.50	
D4	0.40	0.70	
E	4.00	BSC	
E1	3.80 REF		
E2	2.95	3.05	
E3	2.25	2.55	
е	0.65 BSC		
Ĺ	0.12	0.32	
L2	-	0.10	
L3		0.55	

## **GENERIC MARKING DIAGRAM\***

#### **XXXXX** XXXXX XXXXX AYWW= AYWW•

#### w/o ejector pin

# w/ ejector pin

XXXXX = Specific Device Code

= Assembly Location Α

= Year Υ

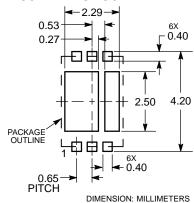
WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking.

## **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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STATUS:	ON SEMICONDUCTOR STANDARD		
NEW STANDARD:			
DESCRIPTION:	WDFN6 2.6X4.0, 0.65P, DUAL FLAG		PAGE 1 OF 2



<b>DOCUMENT</b>	NUMBER:
98AON81958	3F

#### PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY H. INOGUCHI	08 APR 2014
Α	ADDED DIMENSIONS b2, E1 AND L3. REQ. BY C. MACARAIG.	29 DEC 2014
В	ADDED MARKING DIAGRAM WITH EJECTOR PIN. REQ. BY C. MACARAIG.	02 NOV 2016

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