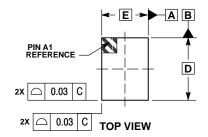




WLCSP12, 1.376x1.025x0.68 CASE 567MW **ISSUE A**

DATE 07 JUN 2017



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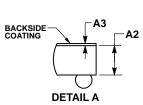
NOTE 3

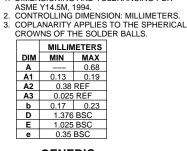
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C A B

0.10 C

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NOTES: 1. DIMENSIONING AND TOLERANCING PER

GENERIC **MARKING DIAGRAM***

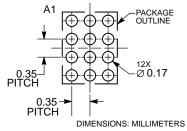


- А = Assembly Location
- L = Wafer Lot
- Y = Year
- = Work Week W
- . = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " .", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED **SOLDERING FOOTPRINT***



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	WLCSP12, 1.376X1.025X0.68		PAGE 1 OF 2





PAGE 2 OF 2

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0	RELEASED FOR PRODUCTION. REQ. BY B. BLAUER.	20 NOV 2015
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