5x Ø **b** C A B

В

 $\oplus | \oplus$

1 2 3

BOTTOM VIEW

0.05

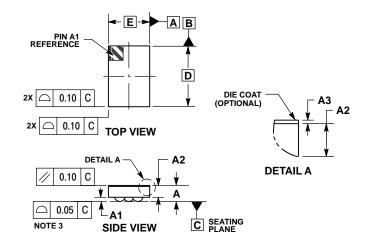
0.03

С

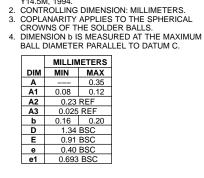


WLCSP5, 1.34x0.91 CASE 567JQ ISSUE A

DATE 09 JUN 2015



e1



NOTES:

GENERIC MARKING DIAGRAM*

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.



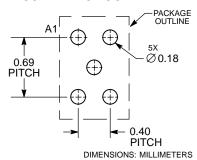
X = Specific Device Code

Y = Year

W = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ■", may or may not be present.

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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